

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
GOVERNMENT COLLEGE OF ENGINEERING, BARGUR – 635104
(An Autonomous Institution Affiliated to Anna University, Chennai)

Minutes of the Fifth Board of Studies meeting of Electrical and Electronics Engineering held on 12th February 2020 at Government College of Engineering, Bargur.

The following agendas are taken for discussion in this meeting.

1. To discuss and get feedback from BoS regarding Answer paper review of Students.
2. To increase the number the number of credits for students appearing in semester Examinations.
3. To enable students to attend the arrear examinations in ODD or EVEN semester without restrictions (For Regulation 2018).
4. To introduce Supplementary Examinations for students who fail so that they will have a chance to clear papers with reference to Anna University guidelines.
5. To discuss about methodology for NPTEL credits and for Library hours.
6. Evaluation of Audit courses.
7. To introduce relative grading.
8. To retain internal marks for arrear Students
9. To modify the syllabus for 18ZES103-Basic Electrical Engineering and 18ZES106- Basic Electrical Engineering Laboratory offered to Mechanical and CSE First year students
10. Any other matter

The following members were present during the meeting.

S.No.	Name	Affiliation
1.	Dr.Sishaj P Simon	University nominee, Associate Professor, Electrical and Electronics Engineering, National Institute of Technology, Tiruchirapalli-620015.
2.	Dr.G.Arunkumar	Subject Expert, Associate Professor, Electrical and Electronics Engineering, VIT University, Vellore-632014

3.	Er.Imayavaramban RM	Representative from Industry Manager-Field Application Engineering, Storage & Electronic Devices Solutions Division, Toshiba India Private Limited, Prestige Terraces, 2nd Floor, #5/1,Union Street,Bangalore-560 001, Karnataka.
4.	Er. M.Malayappan,M.E.,	Alumnus, Director,Pantech Prolabs India Pvt Ltd, 41, Rajeswari Street,Santosh Nagar, Kandanchavadi,Chennai – 600096.
5.	Dr.Nishat Kanvel	Chairman Professor/HOD, EEE, GCE, Bargur.
6.	Mr.K.Mohan	Internal Member , Assistant Professor, EEE, GCE, Bargur
7.	Mr.V.Arivumani	Internal Member, Assistant Professor, EEE, GCE, Bargur
8.	Dr.I.Thangaraju	Internal Member, Assistant Professor, EEE, GCE, Bargur
9.	Dr.R.Francis	Assistant Professor
10.	Dr.V.Adhimoorthy	Assistant Professor
11.	Dr.K.Asokan	Assistant Professor

S.No.	EX OFFICIO MEMBERS
1.	PRINCIPAL, GCE,BARGUR
2.	CONTROLLER OF EXAMINATIONS, GCE,BARGUR

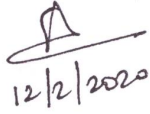
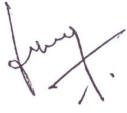

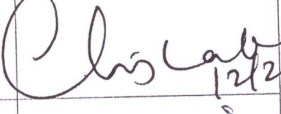
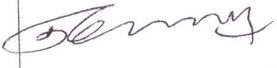
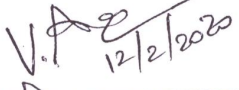
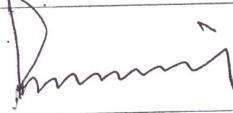
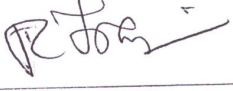
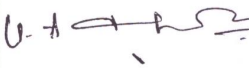
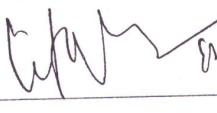
The following member has expressed unable to attend the meeting due to prior commitment to other works.

S.No.	NAME	ADDRESS	AFFILIATION
1.	Dr.N.P.Subramaniam,	Associate Professor, Electrical and Electronics Engineering, Pondicherry Engineering College. Pondicherry-605014.	Subject Expert

The following points were discussed among Board of Studies members and got passed.

1. BoS members suggested collecting minimum (Rs 50) fees for answer paper review.
2. The maximum credit limit is increase to 50 credits for final year students and 44 credits for other students per semester
3. No restrictions for the students to attend the arrear examinations in ODD or EVEN semesters (Regulations 2018).
4. Supplementary examinations for failed students can be conducted and subject limit is restricted to 2 subjects per student. Maximum grade is fixed as 'A'.
5. NPTEL or any online course per semester is made as mandatory for the students and 3 credits will be included in their CGPA. List of NPTEL courses other than core subjects may be finalized by forming committee.
6. If the students obtained NPTEL course certificate for the subjects in the relevant semester (Limited to 3 subjects/semester) and a syllabus of 50% match with current syllabus, they will earn 10 marks as internal with the total internal marks (Max marks 50 marks).
7. Students can take seminar in the audit courses and video may be recorded during the lecture.
8. Comparative analysis will be done for the forthcoming semester results with and without relative grading for the implementation of relative grading system.
9. Internal marks obtained in the current semester will be retained for arrear subjects also and the student may not be entained for reappearing the internal assessment.
10. If a students got below the minimum (25 marks) in their internal marks and if he get pass in their External Exams, the maximum grade will be reduced to the next grade. *not lower than B.*
11. For 18ZES103-Basic Electrical Engineering and 18ZES106- Basic Electrical Engineering Laboratory offered to Mechanical and CSE First year students, the BoS suggested that branch wise syllabus should be framed based on the needs of the CSE and Mechanical department and same was passed.

S.No.	Name	Signature
1.	Dr.Sishaj P Simon University nominee, Associate Professor, Electrical and Electronics Engineering, National Institute of Technology, Tiruchirapalli-620015	<i>S P Simon</i> <i>12/2/2020</i>

2.	Dr.G.Arunkumar Subject Expert, Associate Professor, Electrical and Electronics Engineering, VIT University, Vellore-632014	 12/2/2020
3.	Er.Imayavaramban RM Representative from Industry Manager-Field Application Engineering, Storage & Electronic Devices Solutions Division, Toshiba India Private Limited, Prestige Terraces, 2nd Floor, #5/1, Union Street, Bangalore-560 001, Karnataka.	
4.	Er. M.Malayappan, M.E., Alumnus, Director, Pantech Prolabs India Pvt Ltd, 41, Rajeswari Street, Santosh Nagar, Kandanchavadi, Chennai - 600096.	
5.	Dr.Nishat Kanvel Chairman Professor/HOD, EEE, GCE, Bargur.	 12/2/2020
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11.	Dr.K.Asokan Internal Member , Assistant Professor, EEE, GCE, Bargur	


CHAIRMAN 12/2/2020
BOARD OF STUDIES FOR EEE